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In re Patent Application of

Inventor(s): Donald Paul Richmond II et al.

Group Art Unit: Unassigned

Application No.: 10/718,825

Examiner: Unassigned

Filed: November 21, 2003

Title: Wafer Level Burn-In and Electrical
Test System and Method

Please see attached SECOND PRELIMINARY AMENDMENT.

Ref: 1008.743.301

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PAGE 1/9 *RCVD AT 3/31/2004 7:26:09 PM [Eastern Standard Time]* SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID:650 493 6811 * DURATION (mm:ss):02:14

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application Number	10/718,825
		Filing Date	November 21, 2003
		First Named Inventor	Donald Paul Richmond II et al.
		Art Unit	Unassigned
		Examiner Name	Unassigned
Total Number of Pages in This Submission	9	Attorney Docket Number	1008.743.301

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Technology Center (TC)
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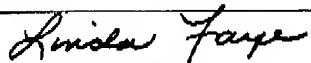
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In re Patent Application of) PATENT APPLICATION
Inventor(s): Donald Paul Richmond II et al.)
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Title: Wafer Level Burn-In and Electrical)
Test System and Method)

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SECOND PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified application as follows:

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